

Form 1449*	Atty. Docket No.: 303.164US3	Serial No. Unknown
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Brett L. Williams	09/510,375
	Filing Date: Herewith	Group: Unknown 2185

## U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<u>12</u>	4,195,770	04/01/1980	Benton, M.K., et al.	371	21.2	10/24/78
<u>12</u>	4,344,156	08/10/1982	Eaton, et al.	365	203	
<u>12</u>	4,484,308	11/20/1984	Lewandowski, et al.	364	900	
<u>12</u>	4,510,603	04/09/1985	Catiller, R.D.	371	21	11/25/83
<u>12</u>	4,513,389	04/23/1985	Devchoudhury, R.N.	364	900	11/12/81
<u>12</u>	4,562,555	12/31/1985	Ouchi, et al.	365	233	
<u>12</u>	4,567,579	01/28/1986	Patel, et al.	365	189	
<u>12</u>	4,575,825	03/11/1986	Ozaki, et al.	365	189	
<u>12</u>	4,603,403	07/29/1986	Toda	365	189	
<u>12</u>	4,618,947	10/21/1986	Tran, et al.	365	230	
<u>12</u>	4,649,522	03/10/1987	Kirsch	365	189	
<u>12</u>	4,685,089	08/04/1987	Patel, et al.	365	233	
<u>12</u>	4,707,811	11/17/1987	Takemae, et al.	365	239	
<u>12</u>	4,788,667	11/29/1988	Nakano, et al.	365	193	
<u>12</u>	4,870,622	09/01/1989	Aria, P.R., et al.	365	230.02	06/24/88
<u>12</u>	4,875,192	10/17/1989	Matsumoto	365	193	
<u>12</u>	4,926,314	05/15/1990	Dhuey, M.	364	200	03/11/87
<u>12</u>	4,993,027	02/12/1991	McGraw, J.M., et al.	371	16.2	09/09/88
<u>12</u>	5,058,066	10/15/1991	Yu	365	189.050	
<u>12</u>	5,126,975	06/30/1992	Handy, J.E., et al.	365	230.01	10/24/90
<u>12</u>	5,237,689	08/17/1993	Behnke, E.	395	700	05/31/90
<u>12</u>	5,253,357	10/12/1993	Allen, G.L., et al.	395	425	06/13/91
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<u>12</u>	5,301,278	04/05/1994	Bowater, R.J., et al.	395	275	04/23/96
<u>12</u>	5,305,284	04/19/1994	Iwase, T.	365	238.5	11/19/92
<u>12</u>	5,307,320	04/26/1994	Farrer, S.M., et al.	365	230.01	09/23/92
<u>12</u>	5,325,330	06/28/1994	Morgan, D.M.	365	189.050	02/11/93
<u>12</u>	5,325,502	06/28/1994	McLaury, L.L.	395	425	05/15/91
<u>12</u>	5,349,566	09/20/1994	Merritt, T.A., et al.	365	233.5	05/19/93
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<u>12</u>	5,373,227	12/13/1994	Keeth, B.	323	313	03/26/93
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\*Substitute Disclosure Statement Form (PTO-1449)

\*\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Sheet 2 of 3

Form 1449*	Atty. Docket No.: 303.164US3	Serial No. Unknown 09/5/0375
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↑	5,513,148	04/30/1996	Zager, P.	365	233	12/01/94
↑	5,522,064	05/28/1996	Aldereguia, A., et al.	395	550	10/01/90
↑	5,526,320	06/11/1996	Zagar, P.S., et al.	365	233.5	12/23/94
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↓	5,640,507	06/17/1997	Lipe, R.	395	183.01	03/02/92
↓	5,713,011	01/27/1998	Satoh, J., et al.	395	556	10/03/94
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**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes   No
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↑	"Rossini, Pentium, PCI-ISA, Chip Set", Symphony Laboratories., entire book,
↑	"4DRAM 1991", Toshiba America Electronic Components, Inc., pp. A-137 - A-159,
↑	"Application Specific DRAM", Toshiba America Electronic Components, Inc., C178, C-260, C 218, (1994)
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↓	"Chips: Intel Corp. intros higher performing PCI chip set for Pentium processors; Triton architecture unlocks Pentium processor power necessary for multitude of applications (Intel Corp.'s 82430FX)", EDGE: Work-Group Computing Report, vol. 6, no. 246, 10(1), (1995)
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## OTHER DOCUMENTS

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W	"Hyper Page Mode DRAM", <u>Electronic Engineering</u> , 66, No. 813., Woolwich, London, GB, pp. 47-48, (September 1994)
↑	"Intel Readies Triton PCI chipset (Intel's 82430FX)", <u>Electronic News</u> , Vol. 41, No. 2050, 68(1), (1995)
↑	"Mosel-Vitellic V53C8257H DRAM Specification Sheet", 20 pgs., (July 2, 1994)
↑	"Pipelined Burst DRAM", <u>Toshiba, JEDEC JC 42.3 Hawaii</u> , (December 1994)
↑	"Samsung Synchronous DRAM", <u>Samsung Electronics</u> , pp. 1-16, (March 1993)
↑	Advanced, "Advanced ZP Expandable Desktop Baseboard", <u>Technical Product Summary, Version 1.0</u> , (1995)
↓	Bursky, D., "Novel I/O Options and Innovative Architectures Let DRAMs Achieve SRAM Performance; Fast DRAMS can be swapped for SRAM Caches", <u>Electronic Design</u> , 41, pp. 55, 56, 60, 62, 64, (July 1993)
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